Enrollment No:	Exam Seat No:	
C.U.SHAH UNIVERSITY		
Summer-2015		
Subject Code: 4TE03AEC1 Subject Name: Analo Course Name: B.TECH(EEE/IC/EE) Semester: 3	og Electronic Circuit Date :5/5/2015 Marks: 70 Time: 2:30 To 5:30	
 Instructions: Attempt all Questions of both sections in same answer book/Supplementary. Use of Programmable calculator & any other electronic instrument prohibited. Instructions written on main answer book are strictly to be obeyed. Draw neat diagrams & figures (if necessary) at right places. Assume suitable & perfect data if needed. 		
SECTION-I		
Q-1(a) Why short circuit protection is required in ICs.(b) Explain diode regulators.(c) Write Full Form of 1. SMPS 2.BJT	2 3 2	
Q-2 (A) Describe analysis of single stage fet amplifier with necessary et (b) write a short note on tuned filter	equations. 7	
OR Q-2		
(A) Draw and explain Hybrid frequency model for CE configuration (B) Draw and explain darlington pair configuration. Q-3	ons with necessary equations.7	
(A) Draw and explain class A amplifier with necessary equations.(B) Draw and Explain class AB amplifier with necessary equations OR	7 7	

(A) Draw and explain class B amplifier with necessary equations.(B) Draw and explain Push Pull amplifier with necessary equations.

Q-3

7 7

SECTION-II

Q-4	
(A) Explain negative feedback for OPAMP.	. 4
(B) Define terms: 1. Voltage Gain 2. Current Gain	4
(c) Draw pin diagaram of IC741 (OPAMP)	2
(d) Why heat sink is required in ICs?	1
Q-5	
(A) Draw and explain non inverting amplifier with necessary equations.	7
(B) Draw and explain ideal charteristic of OPAMP.	7
OR	,
Q-5	
(A) Draw and explain inverting amplifier with necessary equations.	7
(B) Explain any one balancing technque for frequency compensation.	7
0-6	
(a) Explain working of Weinbridge oscillator with suitable diagram.	7
(b) descirbe R.C. phase shift oscilllator with suitable diagram.	7
(b) describe R.C. phase sinit oscillator with suitable diagram.	,
OR	
Q6	
(a) Explain working of Hartley oscillator with suitable diagram	7
(b) Describe colnitts oscillator with suitable diagram	7

